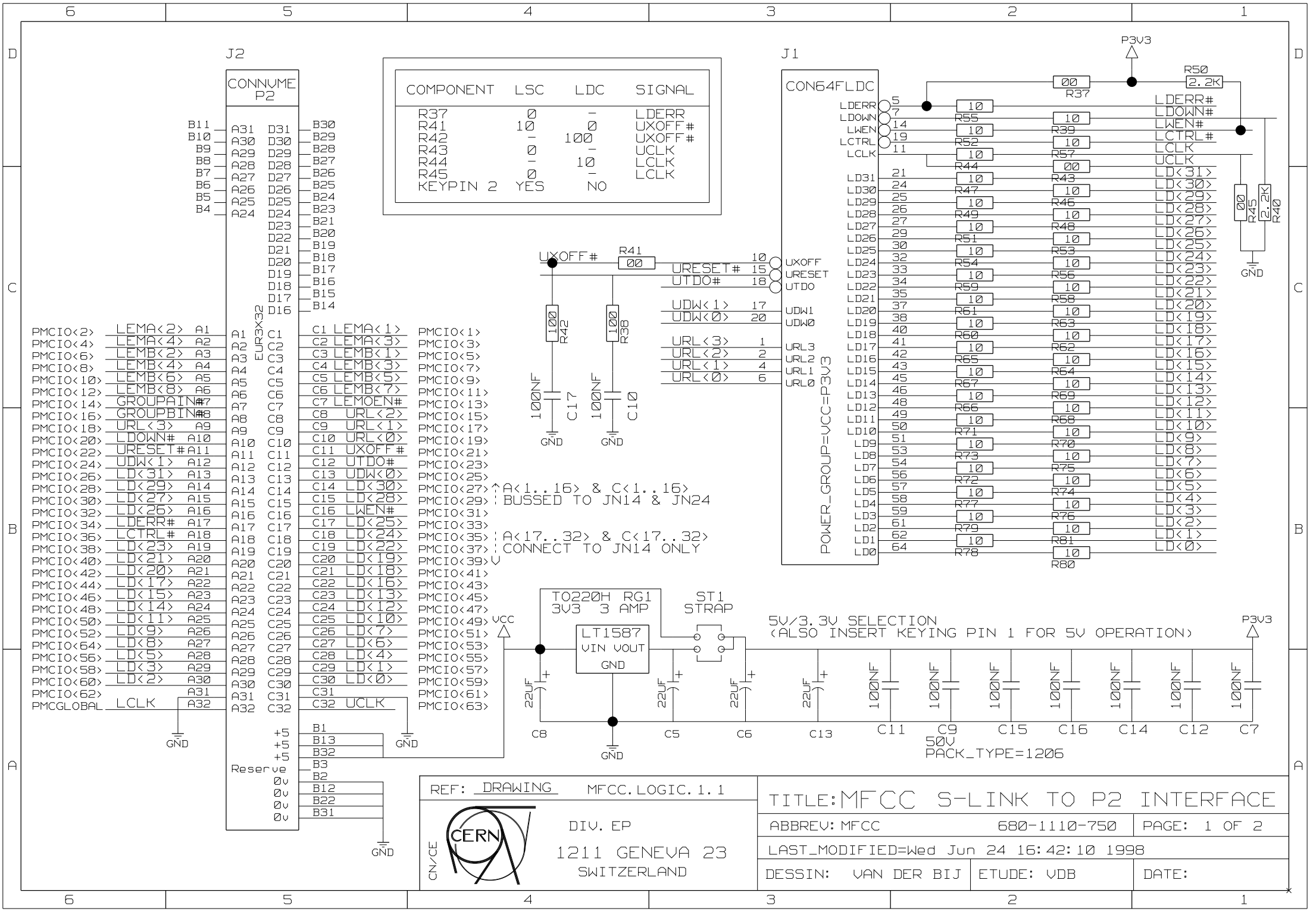

COMPONENT	LSC	LDC	SIGNAL
R37	0	-	LDERR
R41	10	0	UXOFF#
R42	-	100	UXOFF#
R43	0	-	UCLK
R44	-	10	LCLK
R45	0	-	LCLK
KEYPIN 2	YES	NO	

REF: DRAWING MFCC.LOGIC.1.1

CERN CN/CE

DIV. EP

1211 GENEVA 23

SWITZERLAND

TITLE: MFCC S-LINK TO P2 INTERFACE

ABBREV: MFCC | 680-1110-750 | PAGE: 1 OF 2

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DESSIN: VAN DER BIJ | ETUDE: VDB | DATE: