

### 3-row P2 or P3 connector map to one S-LINK LSC using P2-type backplane

		A	B	C
<b>LSC SLOT C</b>	1	DL1 (2)	Vcc (1)	DL0 (2)
	2	DL3 (2)	Gnd (1)	DL2 (2)
	3	Gnd (3)	BL0 (1)	DL4 (2)
	4	UD0	BL1 (1)	Gnd (3)
	5	UD2	BL2 (1)	UD1
	6	UD4	BL3 (1)	UD3
	7	UD6	BL4 (1)	UD5
	8	Gnd (3)	BL5 (1)	UD7
	9	UD8	BL6 (1)	Gnd (3)
	10	UD10	BL7 (1)	UD9
	11	UD12	BL8 (1)	UD11
	12	UD14	Gnd (1)	UD13
	13	Gnd (3)	Vcc (1)	UD15
	14	UD17	BL9 (1)	UD16
	15	UD18	BL10 (1)	Gnd (3)
	16	UD20	BL11 (1)	UD19
	17	UD22	BL12 (1)	UD21
	18	Gnd (3)	BL13 (1)	UD23
	19	UD25	BL14 (1)	UD24
	20	UD26	BL15 (1)	Gnd (3)
	21	UD28	BL16 (1)	UD27
	22	UD30	Gnd (1)	UD29
	23	Gnd (3)	BL17 (1)	UD31
	24	UCTRL#	BL18 (1)	UDW0
	25	UDW1	BL19 (1)	UTEST#
	26	URESET#	BL20 (1)	Gnd (3)
	27	Gnd (3)	BL21 (1)	UWEN#
	28	UCLK	BL22 (1)	Gnd (3)
	29	Gnd (3)	BL23 (1)	LFF#
	30	LDOWN #	BL24 (1)	NC (4)
	31	LRL0	Gnd (1)	LRL1
	32	LRL2	Vcc (1)	LRL3

BLx (1) : Bused Line, user definable signal bused between slots

Gnd (1) : Ground from backplane

Vcc (1) : +5 Volt power supply from backplane

DLx (2) : Direct Line, user definable signal with direct connection from front side to rear side

Gnd (3) : Ground, connect to groundplane and to Gnd pins from row B on both front module and on rear module

NC (4) : Do not connect (corresponds to LDERR# line on LDC connector)

### 3-row P2 or P3 connector map to one S-LINK LDC using P2-type backplane

		A	B	C
<b>LDC SLOT C</b>	1	DL1 (2)	Vcc (1)	DL0 (2)
	2	DL3 (2)	Gnd (1)	DL2 (2)
	3	Gnd (3)	BL0 (1)	DL4 (2)
	4	LD0	BL1 (1)	Gnd (3)
	5	LD2	BL2 (1)	LD1
	6	LD4	BL3 (1)	LD3
	7	LD6	BL4 (1)	LD5
	8	Gnd (3)	BL5 (1)	LD7
	9	LD8	BL6 (1)	Gnd (3)
	10	LD10	BL7 (1)	LD9
	11	LD12	BL8 (1)	LD11
	12	LD14	Gnd (1)	LD13
	13	Gnd (3)	Vcc (1)	LD15
	14	LD17	BL9 (1)	LD16
	15	LD18	BL10 (1)	Gnd (3)
	16	LD20	BL11 (1)	LD19
	17	LD22	BL12 (1)	LD21
	18	Gnd (3)	BL13 (1)	LD23
	19	LD25	BL14 (1)	LD24
	20	LD26	BL15 (1)	Gnd (3)
	21	LD28	BL16 (1)	LD27
	22	LD30	Gnd (1)	LD29
	23	Gnd (3)	BL17 (1)	LD31
	24	LCTRL#	BL18 (1)	UDW0
	25	UDW1	BL19 (1)	UTDO#
	26	URESET#	BL20 (1)	Gnd (3)
	27	Gnd (3)	BL21 (1)	LWEN#
	28	LCLK	BL22 (1)	Gnd (3)
	29	Gnd (3)	BL23 (1)	UXOFF#
	30	LDOWN #	BL24 (1)	LDERR #
	31	URL0	Gnd (1)	URL1
	32	URL2	Vcc (1)	URL3

BLx (1) : Bused Line, user definable signal bused between slots

Gnd (1) : Ground from backplane

Vcc (1) : +5 Volt power supply from backplane

DLx (2) : Direct Line, user definable signal with direct connection from front side to rear side

Gnd (3) : Ground, connect to groundplane and to Gnd pins from row B on both front module and on rear module

## 5-row P2 or P3 connector map to two S-LINK LDCs using P2-type VME64x backplane

		<b>Z</b>	<b>A</b>	<b>B</b>	<b>C</b>	<b>D</b>
<b>Rows A &amp; C LDC1</b>	1	<i>LD1</i>	DL1 (2)	Vcc (1)	DL0 (2)	<i>LD0</i>
	2	Gnd (1)	DL3 (2)	Gnd (1)	DL2 (2)	<i>LD2</i>
	3	<i>LD4</i>	Gnd (3)	BL0 (1)	DL4 (2)	<i>LD3</i>
	4	Gnd (1)	LD0	BL1 (1)	Gnd (3)	<i>LD5</i>
	5	<i>LD7</i>	LD2	BL2 (1)	LD1	<i>LD6</i>
	6	Gnd (1)	LD4	BL3 (1)	LD3	<i>LD8</i>
	7	<i>LD10</i>	LD6	BL4 (1)	LD5	<i>LD9</i>
	8	Gnd (1)	Gnd (3)	BL5 (1)	LD7	<i>LD11</i>
<b>Rows Z &amp; D LDC2</b>	9	<i>LD13</i>	LD8	BL6 (1)	Gnd (3)	<i>LD12</i>
	10	Gnd (1)	LD10	BL7 (1)	LD9	<i>LD14</i>
	11	<i>LD16</i>	LD12	BL8 (1)	LD11	<i>LD15</i>
	12	Gnd (1)	LD14	Gnd (1)	LD13	<i>LD17</i>
	13	<i>LD19</i>	Gnd (3)	Vcc (1)	LD15	<i>LD18</i>
	14	Gnd (1)	LD17	BL9 (1)	LD16	<i>LD20</i>
	15	<i>LD22</i>	LD18	BL10 (1)	Gnd (3)	<i>LD21</i>
	16	Gnd (1)	LD20	BL11 (1)	LD19	<i>LD23</i>
	17	<i>LD25</i>	LD22	BL12 (1)	LD21	<i>LD24</i>
	18	Gnd (1)	Gnd (3)	BL13 (1)	LD23	<i>LD26</i>
	19	<i>LD28</i>	LD25	BL14 (1)	LD24	<i>LD27</i>
	20	Gnd (1)	LD26	BL15 (1)	Gnd (3)	<i>LD29</i>
	21	<i>LD31</i>	LD28	BL16 (1)	LD27	<i>LD30</i>
	22	Gnd (1)	LD30	Gnd (1)	LD29	<i>UDW0</i>
	23	<i>LCTRL#</i>	Gnd (3)	BL17 (1)	LD31	<i>UDW1</i>
	24	Gnd (1)	LCTRL#	BL18 (1)	UDW0	<i>UTDO#</i>
	25	<i>LWEN#</i>	UDW1	BL19 (1)	UTDO#	<i>URESET#</i>
	26	Gnd (1)	URESET#	BL20 (1)	Gnd (3)	<i>UXOFF#</i>
	27	<i>LCLK</i>	Gnd (3)	BL21 (1)	LWEN#	<i>URL0</i>
	28	Gnd (1)	LCLK	BL22 (1)	Gnd (3)	<i>URL1</i>
	29	<i>LDOWN #</i>	Gnd (3)	BL23 (1)	UXOFF#	<i>URL2</i>
	30	Gnd (1)	LDOWN #	BL24 (1)	LDERR #	<i>URL3</i>
	31	<i>LDERR #</i>	URL0	Gnd (1)	URL1	Gnd (1)
	32	Gnd (1)	URL2	Vcc (1)	URL3	Vcc (1)

BLx (1) : Bused Line, user definable signal bused between slots

Gnd (1) : Ground from backplane

Vcc (1) : +5 Volt power supply from backplane

DLx (2) : Direct Line, user definable signal with direct connection from front side to rear side

Gnd (3) : Ground, connect to groundplane and to Gnd pins from row B on both front module and on rear module