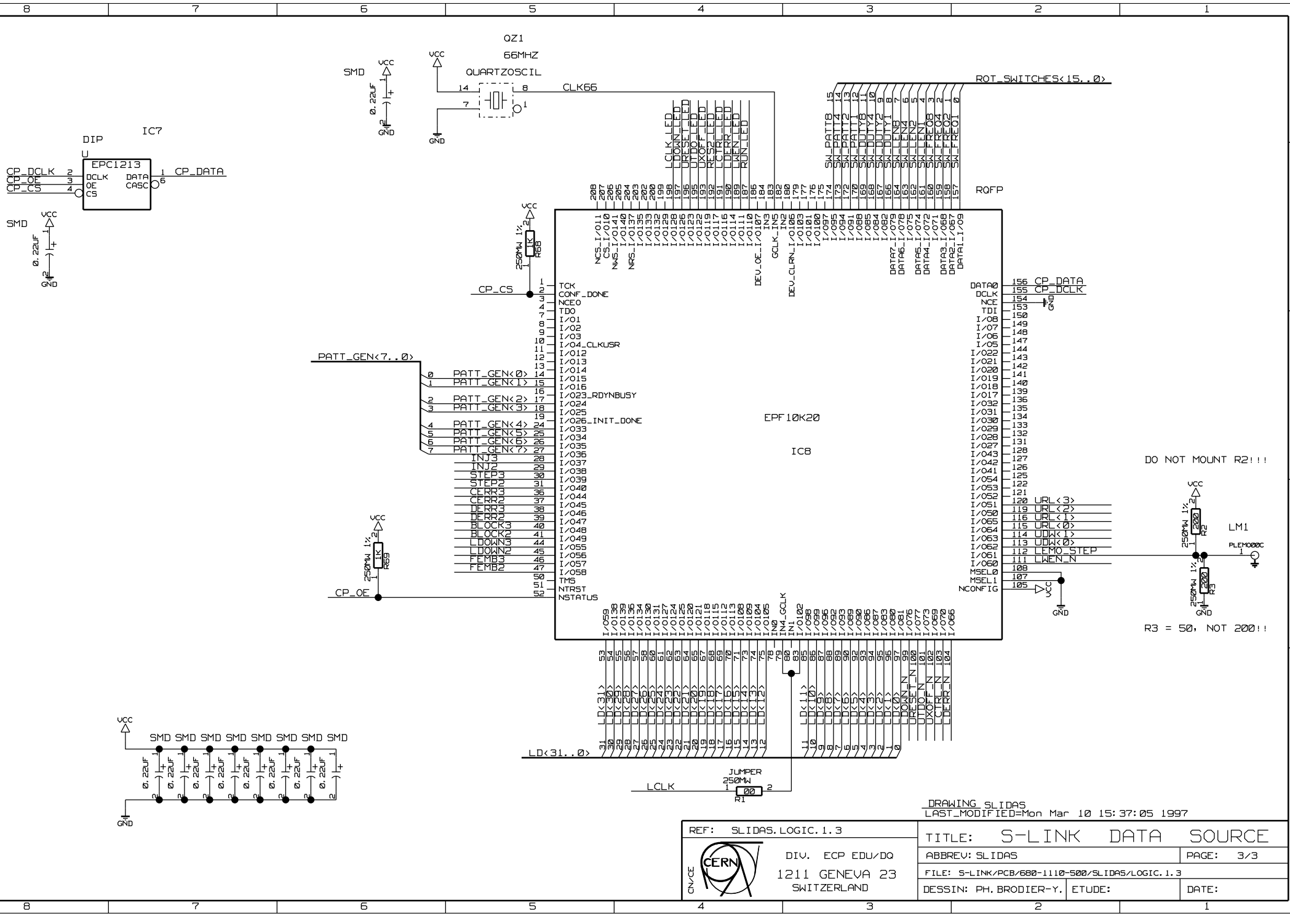
QZ1
66MHZ
QUARTZOSCIL
CLK66
IC7
EPC1213
CP_DCLK
CP_DATA
SMD
0.22UF
ROT_SWITCHES<15..0>
RQFP
EPF10K20
IC8
CP_CS
CP_OE
PATT_GEN<7..0>
LD<31..0>
LCLK
JUMPER
R1
DO NOT MOUNT R2!!!
LM1
R3 = 50, NOT 200!!
DRAWING SLIDAS
LAST_MODIFIED=Mon Mar 10 15:37:05 1997
REF: SLIDAS.LOGIC.1.3
DIV. ECP EDU/DQ
1211 GENEVA 23
SWITZERLAND
TITLE: S-LINK DATA SOURCE
ABBREV: SLIDAS
PAGE: 3/3
FILE: S-LINK/PCB/680-1110-500/SLIDAS/LOGIC.1.3
DESSIN: PH.BRODIER-Y.
ETUDE:
DATE: